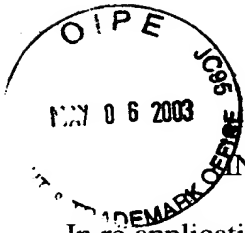


2811



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Gotkis et al.

Application No: 09/821,415

Filed: March 28, 2001

For: SEMICONDUCTOR STRUCTURE  
IMPLEMENTING)LOW-K DIELECTRIC MATERIALS)  
AND SUPPORTING STUBS (As Amended)



) Docket No: LAM2P246

) Examiner: H.K. Vu

) Group Art Unit: 2811

) Date: May 1, 2003

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CERTIFICATE OF MAILING

I hereby certify that this paper (along with any paper referred to as being attached or enclosed) is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, Washington, DC 20231 on May 1, 2003.

Signed: \_\_\_\_\_

Kay Harlow

**INFORMATION DISCLOSURE STATEMENT  
UNDER 37 CFR §§1.97(e)**

Commissioner for Patents  
Washington, DC 20231

Dear Sir:

The references listed in the attached PTO Form 1449, copies of which are attached, may be material to examination of the above-identified patent application. Applicants submit these references in compliance with their duty of disclosure pursuant to 37 CFR §§1.56 and 1.97. The Examiner is requested to make these references of official record in this application.

A statement is hereby made that each item of information contained in the information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this statement. (37 CFR 1.97(e)(1)). A copy of an International Search Report is enclosed herein. If it is determined that any fees are due, the Commissioner is hereby authorized to charge such fees to Deposit Account 50-0805 (Order No. LAM2P246).

Respectfully submitted,  
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Attorney Docket No: LAM2P246



<b>Form 1449 (Modified)</b>  <b>Information Disclosure Statement By Applicant</b>  (Use Several Sheets if Necessary)	Attorney Docket No: U.S. LAM2P246 09/821,415 Applicant: Gotkis et al. Filing Date: Group: March 28, 2001 2811
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### U.S. Patent Documents

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub-class
	A	5,034,799	07/1991	Tomita et al.	357	71
	B	5,828,121	10/1998	Lur et al.	257	522
	C	6,057,224	05/2000	Bothra et al.	438	619
	D					
	E					
	F					
	G					
	H					
	I					
	J					
	K					

### Foreign Patent or Published Foreign Patent Application

Examiner Initial	No.	Document No.	Date	Country or Patent Office	Class	Sub-class	Translation	
							Yes	No
	L	EP0872887A2	21/10/1998	EPO	H01L	23/48		X
	M	63-313896 (and English Abstract)	12/21/1988	JPO	H05K	3/46	X	
	N							
	O							
	P							

### Other Documents

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
	R	Zielinski et al., "Damascene Integration of Copper and Ultra-Low-k Xerogel for High Performance Interconnects", p. 31.7.1 to 31.7.3, IEDM 1997, Texas Instruments, Dallas, TX.
Examiner		Date Considered

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.